

CHALAPATHI INSTITUTE OF ENGINEERING AND TECHNOLOGY Chalapthi Nagar, Lam, Guntur-522034

ECE NEWSLETTER

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

ABOUT CIET

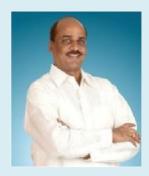
Chalapathi Institute of Engineering & Technology (CIET), is located at Lam, Guntur, 5 Km from the heart of the city. The college was established in the year 2007. It is affiliated to Acharya Nagarjuna University and approved by AICTE, New Delhi. The main aim is to create unique environment conducive to learning and committed to excellence. The college bestows an erudite atmosphere crafted with ethics, to adorn the education of the students. The college is geared up with excellent facilities and state-of-the-art infrastructure. It inculcates research attitude within the students and concurrently nurtures them with industry-demanding caliber.

VISION:

We emphasize the knowledge, skills and experience necessary for students to pursue academic achievement and workforce opportunities. To make the students productive and engaged citizens capable of meeting the complex challenges of a global community

MISSION:

To provide world-class education and research opportunities to the regional community, as well as to the nation. To prepare the students for jobs in the engineering profession, research leading to innovative technologies, partnerships with industry, connecting both locally and globally and pursuing excellence in every endeavor



Sri. Y. V. Anjaneyulu Chairman



Sri D. Vinay Kumar Director



Dr. P. Suresh Babu Principal

ABOUT ECE DEPARTMENT

The Department of Electronics and Communication Engineering lays emphasis on Teaching and Research activities in diversified areas thereby, moulding the students to be Analytical Thinkers, Skilled Communicators and Ethical Leaders.

VISION

We emphasize up to date technical knowledge, analytical and practical skills necessary for the students to meet the requirements of modern industrial society.

MISSION

To provide research opportunities and good education to meet the demands of electronics and communications industry. To prepare the students for jobs, research leading to innovative technologies and partnerships in electronics industry.

HOD's Message

It gives very content that as a forerunner the department of Electronics & Communication Engineering is releasing its Volume I, first issue of ECE Newsletter 2013-14. The major strength of the department is the amalgamation of our qualified and dedicated faculty who understand the rhythm of the students for their academic excellence. Our under graduate and post graduate program students are equally showing their enthusiasm towards extracurricular and co-curricular activities contemplate them towards placement and higher education. Further, it gives satisfaction to bring out the update information about the department newsletter blended with technical and non-technical segment that is rich in contents. Finally, department would like to convey sincere thanks to the management for their continuous support and admiration.



Dr. M. Satya Sai Ram
Professor & HOD

FACULTY

S.No.	Faculty Name	Designation
1	Dr.M.Satya Sai Ram	Professor &HOD
2	Mr.L. Ravi Chandra	Assistant Professor
3	Mr.B.Venkateshwarao	Assistant Professor
4	Mr.K.Anil Kumar	Assistant Professor
5	Mr.R.Jagadeesh Chandra prasad	Assistant Professor
6	Mrs.N.Uma Kumari	Assistant Professor
7	Mrs.N.Seetha Ratna kumari	Assistant Professor
8	Mr.N.Shiva Naga Raju	Assistant Professor
9	Ms.N.Prathima	Assistant Professor
10	Mrs.K.Vijayalakshmi	Assistant Professor
11	Mrs. M.Vasujadevi	Assistant Professor
12	Mr.T.Rajashekar	Assistant Professor
13	Ms.M. Kalpana	Assistant Professor
14	Mrs.N.Prashanthi	Assistant Professor

DEPARTMENT ACTIVITIES

➤ Department of Electronics and Communication Engineering conducted a ""Computer Networks Mobile Ad-hoc Networks and Wireless Sensor Networks" by Dr. Garimella Ram Murthy, Professor IIIT, Hyderabad on 2/7/2013



➤ Department of Electronics and Communication Engineering conducted a "Antennas for Millimeter Wave Applications" by Dr. M. Venkata Siva Prasad, Professor, Department of ECE, RVR & JC on 11/12/2013

FACULTY ACHIEVEMENTS

- ➤ Ms.M.Kalpnachoudary, Assistant Professor Published a Paper titled "Implementation of running average background subtraction algorithm in FPGA for image processing application" at IJCA, Vol.73 in July 2013
- ➤ Dr. M.Satya Sai Ram, HOD Presented a Paper titled "Handwritten Digit Recognition with MLP neural network by using back propagation training and tunneling algorithm" at International Conference on Navigational Systems and Signal Processing Applications in December 2013
- ➤ Dr. M.Satya Sai Ram, HOD Presented a Paper titled "An optical flow approach to robust face recognition under expression variations" at International conference on Navigational Systems and Signal Processing Applications in December 2013