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# ECE NEWSLETTER



CHALAPATHI INSTITUTE OF  
ENGINEERING & TECHNOLOGY

**CHALAPATHI INSTITUTE OF  
ENGINEERING AND TECHNOLOGY**  
Dept. of Electronics & Communication Engineering

**Chief Editor:** Dr. M. Satya Sai Ram, HOD

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## ABOUT CIET

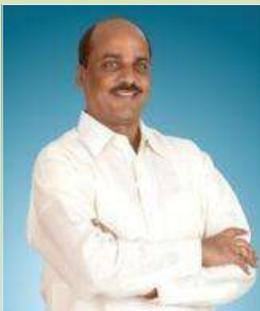
Chalapathi Institute of Engineering & Technology (CIET), is located at Lam, Guntur, 5 Km from the heart of the city. The college was established in the year 2007. It is affiliated to Acharya Nagarjuna University and approved by AICTE, New Delhi. The main aim is to create unique environment conducive to learning and committed to excellence. The college bestows an erudite atmosphere crafted with ethics, to adorn the education of the students. The college is geared up with excellent facilities and state-of-the-art infrastructure. It inculcates research attitude within the students and concurrently nurtures them with industry-demanding caliber.

### VISION:

To emerge as an Institute of Excellence for Engineering and Technology and provide world-class education and research opportunities to the students catering the needs of society

### MISSION:

Establishing a state-of-the-art Engineering Institute with continuously improving infrastructure and produce students with innovative skills and global outlook .



**Sri. Y. V. Anjaneyulu**  
Chairman



**Sri Y.V. Sujith Kumar**  
Secretary



**Sri D. Vinay Kumar**  
Director



**Dr.P.Pandarinath**  
Principal

## ABOUT THE DEPARTMENT

The Department of Electronics and Communications Engineering has been functioning since the beginning of the college i.e., Academic Year 2007-08 with an intake of 60. Presently the intake of the ECE department is 120. The department has well qualified, experienced and dedicated faculty and three faculty are doctorates and some of them are pursuing Ph.D in different streams. ECE department is offering M.Tech course in VLSI and Embedded systems from A.Y. 2012-13 with an intake of 18.

### VISION

Emphasize best practices towards technical knowledge, analytical and practical skills necessary to meet the demands of Electronics and Communication Engineering industry.

### MISSION

DM1. Provide Quality education with state of art laboratories.

DM2. Collaborations with industries/ organizations and provide trainings/ Internships to meet the demands of industry.

DM3. Create a research environment of excellence.

### HOD'S Message

It gives very content that as a forerunner the department of Electronics & Communication Engineering is releasing its October-December 2016 issue of ECE Newsletter 2016-17. The major strength of the department is the amalgamation of our qualified and dedicated faculty who understand the rhythm of the students for their academic excellence. Our under graduate and post graduate program students are equally showing their enthusiasm towards extracurricular and co-curricular activities contemplate them towards placement and higher education. Further, it gives satisfaction to bring out the update information about the department newsletter blended with technical and non-technical segment that is rich in contents. Finally, department would like to convey sincere thanks to the management for their continuous support and admiration.



**Dr. M. Satya Sai Ram**  
Professor & HOD

## FACULTY DETAILS

S.NO	Name of the Faculty	Designation
1	DR M.SATYA SAI RAM	Professor & HOD
2	DR B ANAND KRISHNA	Professor
3	MD SHAFIVULA	Associate Professor
4	DR.N.RAMESH BABU	Associate Professor
5	M.SUBRAHMANYESWARA RAO	Associate Professor
6	M.SURESH	Assistant Professor
7	N RAJESH BABU	Assistant Professor
8	B.VENKATESWARA RAO	Assistant Professor
9	L.RAVI CHANDRA	Assistant Professor
10	R.JAGADEESH CHANDRA PRASAD	Assistant Professor
11	K.ANIL KUMAR	Assistant Professor
12	K.VIJAYA LAKSHMI	Assistant Professor
13	M.SIRISHA	Assistant Professor
14	P.MANI VARA KUMAR	Assistant Professor
15	SK.MASTHAN	Assistant Professor
16	N.UMA KUMARI	Assistant Professor
17	M.KALPANA CHOWDARY	Assistant Professor
18	K.RAJESH	Assistant Professor
19	N.NAGA RAJU	Assistant Professor
20	SK.ASMA	Assistant Professor
21	P.BALA PRASANTHI	Assistant Professor
22	K.BRAHMENDRA	Assistant Professor
23	M KOTESWARA RAO	Assistant Professor
24	A SURESH	Assistant Professor
25	V.PADMAJA	Assistant Professor
26	Y VISHNU VARDHAN	Assistant Professor

## DEPARTMENT ACTIVITIES

- **Inauguration of ISTE student chapter** on 03<sup>rd</sup> October 2016. Dr. M. Satya Sai Ram (HOD-ECE), Dr. Suresh Babu(Principal), Sri Y. V Anjaneyulu(Chairman) And Dr. K. Ramanjaneyulu (Professor, PVPSIT, Vijayawada) from left to right (sitting) IETE Student co-ordinators R. Shruthi, R. Bharathi, K. Sai Bharghavi, M. Malleswari, K. Nagendra Babu, T. Seshu Babu, T. Sai Gokul, G. Hari Krishna(from left to right standing backside)



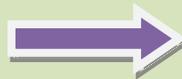
- ❖ **3- Day National Level Faculty Development program** on Embedded Systems was conducted on 3<sup>rd</sup>, 4<sup>th</sup> and 5<sup>th</sup> October 2016. Program was organized in association with ISTE student chapter and SAMVID ECE association. 48 faculty from different colleges and 95 students benefited from the Faculty development program.



Dr. P. Rajesh Kumar, HOD ECE,  
PVP Siddartha Institute of  
technology addressing the faculty



Garimella Rammurthy, Professor IIIT  
Hyderabad, delivering lecture on  
Embedded systems





*Presenting Memo to the chief guest  
(president of ISTE Vijayawada chapter )*

**TOURS**

❖ II/IV B. Tech(ECE) Students Visited Efftronics, Vijayawada, Undavalli Caves on 05th Nov 2016.



*II/IV B. Tech students at Undavalli  
caves with faculty members Mr. N.  
NagaRaju garu and Ms. V. Padmaja*

*II/IV B.Tech students listening to the  
demonstration by Efftronics employee*



❖ IV/IV B. Tech(ECE) Students Visited Cyclone Detection Radar Station , Machilipatnam on 23<sup>rd</sup> December 2016 and III/IV students visited Doordarshan Kendra on 26<sup>th</sup> December 2016



*IV/IV students at Cyclone Detection Radar Station, Machilipatnam with ECE Faculty.*

*Radar station official is explaining the operation of Cyclone Detection Radar*



*III/IV Year students at Door darshan kendram, Kondapalli.*

### Faculty Appreciation Program on 17<sup>th</sup> Nov 2016

- **Dr. Kodela Siva Prasad, Speaker, AP Legislative Assembly and Dr. A. Rajendra Prasad, Vice Chancellor, Acharya Nagarjuna University** visited the college on 17<sup>th</sup> November 2016 for the Faculty appreciation program. He appreciated the faculty who are doing their best services to the society. Totally 9 faculty from ECE department received the appreciation certificate and cash prize of Rs.2500 for achieving 100% results in end examinations



*Mr. B. Venkateswara Rao, Assistant Professor receiving Faculty appreciation certificate from Dr. Kodela Shiva Prasad , Speaker, AP Legislative Assembly.  
ANU Vice chancellor Dr.A. Rajendra Prasad ,Chairman Sri Y. V Anjaneyulu , Secretary Y. Sujith Kumar and Principal Dr. P. Suresh Babu is also present on the dias.*

*M. Sirisha, Assistant professor receiving Faculty appreciation certificate from Dr. Kodela Shiva Prasad, Speaker, AP legislative Assembly. Vice chancellor Dr. A. Rajendra Prasad is present in the photo*



- II/IV B.Tech ECE students visited Efftronics, Vijayawada on 20/08/2016.



*II/IV B.Tech ECE students near the Efftronics, Vijayawada*

II/IV B.Tech ECE students near Vundavalli caves.



- Final year ECE visited to Doordharshan Kendra, Vijayawada on 08-09-2016



- III/IV B. Tech(ECE) Students Visited FM Radio Station, Vijaawada, Undavalli Caves, Radio Transmitter in Namburu on 27<sup>th</sup> Sep 2016



At Undavalli Caves



At FM Radio Station

- Department of ECE conducted organized one day guest lecture on “Entrepreneur skill development program” by Sri G. Sudarshan, Shasrith Enterprises Hyderabad on 02-07-2016



- Department of ECE organized a three day workshop on “PCB Design” by Sri. Finney Daniel, CESD Vijyawada from 25-07-2016 to 27-07-2016



## PAPER CLIPPING



## FACULTY ACHIEVEMENTS

### FACULTY DEVELOPMENT PROGRAMS (FDPS):

1. **Dr. M. Satya Sai Ram** attended Three Day National Level Faculty Development Program on “Embedded Systems” at Chalapathi Institute of Engineering and Technology, in association with ISTE NEW DELHI during 3rd – 5th Oct 2016.
2. **M. Subrahmanyeswara Rao** participated in a Three day national Level faculty development program on embedded systems in association with ISTE –Delhi during 3<sup>rd</sup> -5<sup>th</sup> October 2016 at Chalapathi Institute of engineering and technology, Lam, Guntur
3. **B. Venkateswara Rao** participated in 2 Day FDP on “UBUNTU Essentials” organized by Department of CSE held at Chalapathi Institute of Engineering and Technology, Guntur from 30th Sep to 01st Oct 2016
4. **B.Venkateswara Rao**, Assistant Professor attended a Faculty Development Programme on “Communication Technologies& Applications” at V.R. Siddhartha Engineering College, Vijayawada (22-06-2016 to 02-07-2016)
5. **B. Venkateswara Rao** participated in 2 Day Workshop on “Embedded Systems” held at NRI Institute of Technology, Guntur from 28th July 2016 to 29th July 2016.
6. **K. Anil Kumar**, Assistant Professor attended a Faculty Development Programme on “Communication Technologies& Applications” at V.R. Siddhartha Engineering College, Vijayawada (22-06-2016 to 02-07-2016)
7. **K.Anil Kumar** attended seven days faculty Development program on ‘Communication technologies & applications organized by E & ICT Academy held at KITS college of engg in July2016.
8. **K.Vijaya Lakshmi**, Assistant Professor attended a Workshop on “Design and Development of Small Satellites” at Bapatla Women's Engineering College, Bapatla (2207-2016 & 23-07-2016)
9. **K. Vijaya Lakshmi** attended Two day National Level workshop entitled “Inter Disciplinary Worshop on Design and Development of small satellites ” conducted in Bapatla Women’s Engineering College, Bapatla on 22nd&23rd July, 2016.
10. **K. Vijaya Lakshmi** attendedThree-day day National Level workshop on “PCB DESIGN” conducted by Center for Electronics system Design and Department of ECE, Chalapathi institute of Engineering and Technology, Guntur on 25th July to 27<sup>th</sup> July 2016.
11. **M. Kalpana Chowdary** attendedThree-day day National Level workshop on “PCB DESIGN” conducted by Center for Electronics system Design and Department of ECE, Chalapathi institute of Engineering and Technology, Guntur on 25th July to 27<sup>th</sup> July 2016.

12. **M. Sirisha** Participated in “EMBEDDED SYSTEMS” organized by Chalapathi Institute of Engineering & Technology during 3rd, 4th & 5th October 2016.
13. **M.Sirisha**, Assistant Professor attended a Faculty Development Programme on “Custom IC Design ASIC Implementation using Cadence EDA Tools” at Dhanekula Institute of Engineering and Technology (03-08-2016 to 05-08-2016)
14. **M. Kalpana** attended a three days FDP program on “EMBEDED SYSTEMS” conducted by CIET in Association with ISTE during 3rd,4th and 5th October 2016
15. **M.Kalpana Chowdary**, Assistant Professor attended a Faculty Development Programme on “Custom IC Design ASIC Implementation using Cadence EDA Tools” at Dhanekula Institute of Engineering and Technology (03-08-2016 to 05-08-2016)
16. **P. Manivara Kumar** attended a 3-day Faculty Development Program “Design and Analysis of RF antennas using HFSS” conducted by Lakireddy Balireddy College of Engineering, Mylavaram. 29-11-2016 to 01-12-2016
17. **P. Manivara Kumar** attended a 3-day Faculty Development Program “Embedded Systems”, in association with ISTE, New Delhi conducted by Chalapathi Institute of Engineering and Technology, Guntur during 3rd , 4th and 5th October 2016.
18. **P. Manivara Kumar**, Assistant Professor attended a Workshop on “Design and Development of Small Satellites” at Bapatla Women's Engineering College, Bapatla (2207-2016 & 23-07-2016)
19. **P. Manivara Kumar** attended a 6-day Faculty Development Program “Practical RF Antenna Design” held at VNR Vignana Jyothi Institute of Engineering and Technology, Hyderabad From 2-08-2016 to 9-08-2016
20. **K. Rajesh** participated in 6 Day FDP on “Recent trends in Signal processing” organized by E&ICT Academy, NIT Warangal held at KKR&KSR Institute of Technology & Sciences, Guntur from 31st October 2016 to 5th November 2016
21. **K. Rajesh** participated in 3 Day FDP on “Embedded Systems” held at Chalapathi Institute of Engineering and Technology, Guntur from 3rd October 2016 to 5th October 2016 during the
22. **Aala Suresh** participated in 3 days national level FDP on “EMBEDDED SYSTEMS” organized by CIET, Lam, Guntur in association with ISTE from 3rd to 5th oct 2016.
23. **Aala Suresh** participated in 2 days national level FDP on “UBUNTU ESSENTIALS” organized by CIET, Lam, Guntur from 30td Sep to 1st Oct 2016.
24. **Sk. Asma** Participated in 3-day National Level Faculty Development Program on “Embedded System” organized ECE Dept in association with ISTE at Chalapathi Engineering college on 3<sup>rd</sup> to 5<sup>th</sup> October 2016.
25. **K. Vijaya Lakshmi** attended Three-day day National Level Faculty Development program on “Embedded systems” conducted by Department of ECE, Chalapathi institute of Engineering and Technology, Guntur on 3<sup>rd</sup> October to 5<sup>th</sup> October 2016.
26. **K. Vijaya Lakshmi** attended one week National Level Faculty Development program on “IOT and its Real time applications” conducted by KKR & KSR Institute of Technology and sciences ,Vinjanampadu on 25th March to 1st April 2017.
27. **P. BalaPrasanthi** participated in 3-day National Level Faculty Development Program on “Embedded System” organized ECE Dept in association with ISTE at Chalapathi Institute of Engineering & Technology from 3th -5<sup>th</sup> October 2016.
28. **P. Kanvitha** Participated in the 3-Day National Level Faculty Development Program on “EMBEDDED SYSTEMS” Conducted by Chalapathi Institute of Engineering and Technology in association with ISTE, New Delhi during 3rd,4th and 5th October 2016.

29. **P. Soujanya** participated in the 3-Day National Level Faculty Development Program on “EMBEDDED SYSTEMS” Conducted by Chalapathi Institute of Engineering and Technology in association with ISTE, New Delhi during 3rd,4th and 5th October 2016.

### CERTIFICATIONS:

1. **N.Naga Raju**, Assistant Professor qualified Microsoft Technology Associate Exam in August 2016.
2. **K. Anil Kumar**, Assistant Professor qualified Microsoft Technology Associate Exam in August 2016
3. **R.Jagadeesh Chandra Prasad**, Assistant Professor qualified Microsoft Technology Associate Exam in August 2016
4. **M.Sirisha**, Assistant Professor qualified Microsoft Technology Associate Exam in August 2016
5. **V.Padma**, Assistant Professor qualified Microsoft Technology Associate Exam in August 2016

### WORKSHOPS

1. **M. Subrahmanyeswara Rao** participated in one day work shop on “ UAV’s/DRONES – Technological advances and Innovations “ organized by Industry relations committee organized by IEEE Hyderabad on 5<sup>th</sup> November 2016
2. **K. Vijaya Lakshmi** attended Two day National Level workshop entitled “Inter Disciplinary Workshop on Design and Development of small satellites ” conducted in Bapatla Women’s Engineering College, Bapatla on 22nd&23rd July, 2016.
3. **K. Vijaya Lakshmi** attended Three-day day National Level workshop on “PCB DESIGN” conducted by Center for Electronics system Design and Department of ECE, Chalapathi institute of Engineering and Technology, Guntur on 25th July to 27<sup>th</sup> July 2016.
4. **M. Kalpana Chowdary** attended Three-day day National Level workshop on “PCB DESIGN” conducted by Center for Electronics system Design and Department of ECE, Chalapathi institute of Engineering and Technology, Guntur on 25th July to 27<sup>th</sup> July 2016.
5. **B. Venkateswara Rao** participated in 2 Day Workshop on “Embedded Systems” held at NRI Institute of Technology, Guntur from 28th July 2016 to 29th July 2016.
6. **K. Anil Kumar** Participated in the workshop on ‘Embedded technologies” held at NRI Engineering College, Guntur in Aug 2016.
7. **K. Anil Kumar** Participated in the workshop on “PCB Design” held at CIET, Lam, Guntur in July 2016.
8. **P. Manivara Kumar** Attended a Workshop on Design and development of Small Satellites conducted at Bapatla Women’s Engineering College on 22nd and 23rd July 2016
9. **Sk. Asma** Participated in 3-Day Workshop on” Printed Circuit Board Design” conducted by Centre for Electronics System Design at Chalapathi Engineering College on 25th – 27th July 2016.
10. **M. Koteswara Rao** attended the one week workshop on “RECENT ADVANCEMENTS IN VLSI TECHNOLOGY & DESIGN USING EDA TOOLS” being organized by the department of Electronics and Communication Engineering, JNTUK, Kakinada, during 20th to 24th July, 2016.
11. **Aala Suresh**, Assistant Professor of ECE dept, Participated in 3 days workshop on “PCB Design” organized by Department of ECE, CIET Lam and Center for Electronics Systems Design, held on 25<sup>th</sup> July to 27<sup>th</sup> July 2016.
12. **Aala Suresh**, Assistant Professor of ECE dept, attended one day **work Shop** on “Electronic Test & Measurement Equipment” organised by IQAC of PVPSIT in association with Synergy measurement Technologies on 11/11/2016.