

CHALAPATHI INSTITUTE OF ENGINEERING AND TECHNOLOGY



(Approved by AICTE, Affiliated To Acharya Nagarjuna University)

Chalapathi Nagar, LAM, Guntur-522034

Accredited by NBA NAAC with 'A' Grade



NEWS LETTER

Department of Electrical and Electronics Engineering

WELCOME: "The Newsletter Welcomes You for the Volume-6, Issue-1, Jan- June 2019"

ABOUT CIET:

Chalapathi Institute of Engineering & Technology (CIET) is located at Lam, Guntur, 5 Km from the heart of the city. The college was established in the year 2007. It is affiliated to Acharya Nagarjuna University and approved by AICTE, New Delhi. The main aim is to create unique environment conducive to learning and committed to excellence. The college bestows an erudite atmosphere crafted with ethics, to adorn the education of the students. The college is geared up with excellent facilities and state-of-the-art infrastructure. It inculcates research attitude within the students and concurrently nurtures them with industry-demanding caliber.

VISION: We emphasize the knowledge, skills and experience necessary for students to pursue academic achievement and workforce opportunities. To make the students productive and engaged citizens capable of meeting the complex challenges of a global community

MISSION: To provide world-class education and research opportunities to the regional community, as well as to the nation. To prepare the students for jobs in the engineering profession, research leading to innovative technologies

ABOUT EEE DEPARTMENT:

The Department of Electrical and Electronics Engineering lays emphasis on Teaching and Research activities in diversified areas thereby, moulding the students to be Analytical Thinkers, Skilled Communicators and Ethical Leaders.

VISION: To cater to the needs and aspirations of the people and their development, reach to the world through state of art technologies of electrical and electronics engineering and to serve the society at large.

MISSION:

- To provide the necessary domain expertise and infrastructure to the students.
- To make available the advanced laboratories, application oriented engineering principles for students and researchers.
- To offer research and industry orientation to become successful service oriented technocrats.

HOD's Message

I am happy to note that the magazine brought out in our EEE department is of good quality and taste. Hearty congratulations to the editorial team. The Department's main objective is to conduct competitive research and deliver high quality teaching. We want to develop graduate engineers with the skills, knowledge and imagination to help shape our country. The zeal and fervor with which the department is working will surely achieve many more things in the future as 'Research Centre of Excellence', 'Accreditation', and 'Autonomy'.



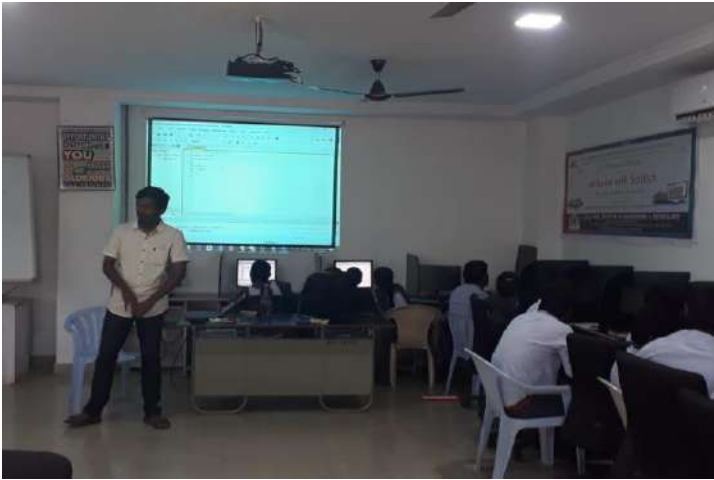
Dr. K. Ravi Chandrudu
Professor & HOD

Our greatest weakness lies in giving up. The most certain way to succeed is to try just one more time

Thomas A. Edison

DEPARTMENT ACTIVITIES

1. The department of EEE organized a three day workshop on “Arduino with scratch” from 23rd to 25th January.



Mr.U.Sumanth, Technical trainer, APSSDC was given the training to students.

2. The students of II/IV B.tech EEE visited “V.T.P.S” Vijayawada, on 2nd March, 2019 along with two faculty members.



Industrial visit of “V.T.P.S” Vijayawada

3. The students of II/IV B.tech EEE visited “Vizag steel plant”, Vizag, on 8th March, 2019 along with two faculty members.



Industrial visit of Vizag steel plant, Vizag

FACULTY ACHIEVEMENTS

International Conferences:

1. **Mr.T.Venkatesh**, attended an International Conference on “Technological Emerging Challenges (ICTEC 2019)” on 15th & 16th March 2019 at Tirumala Engineering College.

STUDENT ACHIEVEMENTS

1. The students of EEE department are participated in various Co-circular activities, as

S. No	Name of The Student	Regd No	Class & Section	Name Of The Event	Venue Name	Prize Won
1	P.Sireesha	L17EE1217	III/IV	Tennikoit	St. Mary's group of Institutions	2 nd
					Sri Mittapalli College of Engineering	
					VIVA VVIT	1 st
					CIET	2 nd
					RVR&JC college of engineering	2 nd
2	Sk.Sai Karimulla	L17EE1215	III/IV	Singing	KITS	2 nd
3	Sk.Yasin Sardar	Y17EE1240	II/IV	Volley Ball	St. Mary's group of Institutions	1 st
					Narasaraopeta Engineering college	2 nd
4	G Prakash Rao	L18EE1251	II/IV	Chess	Sri Chundi Ranganayakulu Engineering College	1 st
					CIT	2 nd

PLACEMENTS

1. The following students of IV/IV EEE students are selected on varies companies in campus selections as

S.No	Name of Student	Register No	On/Off Campus	Name of The Employer	Drive Date
1	Banka Jagadeesh	Y15EE1204			
2	Mutyala Teja Sri	Y15EE1219	On Campus	Adwait Algorithm	11/02/2019
3	Nelluri Renuka	Y15EE1221			
4	Bellam Gopi	Y15EE1205	On Campus	CSS Corp	15/03/2019
5	Bommiseti Gopi	Y15EE1208			



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Editorial Board

- Chief Editor:** Mr.K.Murali Krishna Raju, Assistant Professor, Dept of EEE
Associate Editor(s): Mr. K.Rajesh Kumar, Assistant Professor. Dept of EEE
Student Members: Mr.Ch.V.V.N. Yaswanth (L16EE1231)
 Mr.Ch.Dhanunjaya (L16EE1232)